

Appl. No. 10/695,813  
Amendment dated: October 19, 2006  
Reply to OA of: April 19, 2006

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Claim 1 (currently amended). A driving circuit for solving color dispersion, implemented in a flat panel display with a plurality of display cells, the driving circuit comprising:

a coding unit, to generate a plurality of coded data according to a plurality of characteristic curves;

a reference voltage generator, to receive the coded data, convert the coded data from digital to analog, and generate a plurality of reference voltages; and

a driving unit, to receive the reference voltages and accordingly drive the display cells;

wherein the plurality of characteristic curves are Gamma curves respectively for three primary colors R, G, B.

Claim 2 (original). The driving circuit as claimed in claim 1, wherein the reference voltage generator further comprises a plurality of digital-to-analog converters for digital to analog conversion.

Claim 3 (currently amended). The driving circuit as claimed in claim 2, wherein the digital-to-analog converters input the coded data through sample/latch the reference voltage generator further comprises a plurality of sample/latch units for receiving the coded data, sampling/latching the coded data, and transmitting the coded data to the plurality of digital-to-analog converters.

Appl. No. 10/695,813  
Amendment dated: October 19, 2006  
Reply to OA of: April 19, 2006

Claim 4 (original). The driving circuit as claimed in claim 2, wherein each digital-to-analog converter inputs the coded data through a plurality of control signal lines.

Claim 5 (original). The driving circuit as claimed in claim 1, wherein the reference voltage generator further comprises:

- a plurality of sample/latch circuits, to receive the encoded data and apply the encoded data received to sample/latch processing for output;

- a plurality of digital-to-analog converters, each having a plurality of control signal lines to perform digital to analog conversion according to the encoded data which is outputted by the sample/latch circuit and received by the control signal lines, thereby obtaining the reference voltages; and

- a plurality of buffers, to receive the reference voltages, enhance their output amplitudes, and output the reference voltages enhanced to data drivers.

Claim 6 (canceled).

Claim 7 (original). The driving circuit as claimed in claim 1, wherein the driving unit is a data driver.

Claim 8 (canceled).